



The 28th IEEE International Symposium On Field-Programmable Custom Computing Machines

Workshop:

Intel FPGA Clouds for Academic Research and Teaching

Time: May 6, 11:00 AM – 12:00 PM CDT

Organizer: Intel

Meeting Software & Link: [Webex](#)

Register Here: [Google Sheets](#)



With online learning a fact of life for the short term, this workshop presents Intel FPGA cloud services to keep students and researchers learning, innovating and utilizing FPGAs to solve challenging heterogeneous computing problems. Intel cloud services offer academics and researchers access to servers with the latest Xeon CPUs and one or more Intel FPGA accelerator cards per node. Leading edge development tools and frameworks facilitate productive research and teaching. Supported development flows include: RTL, HLS, OpenCL, DPC++/OneAPI and OpenVino overlays. We will provide an overview of how to register to acquire login access, and demonstrate various workload compilations, setup and execution. There is no cost to utilize these cloud services. Intel looks forward to serving your FPGA training and research needs.

Become part of our growing Intel Academic Program by [signing up](#) today for access to board donations and free development tool licenses.

Our FPGA Cloud is open for free [signup](#) to take advantage of our latest Software Tools and FPGA Acceleration Hardware.

Check out our free online teaching and training materials through our [Academic Website](#).